



Europäisches Patentamt

European Patent Office

Office européen des brevets

(11) Publication number:

0 358 582

A2

(12)

EUROPEAN PATENT APPLICATION

(21) Application number: 89420322.3

(51) Int. Cl. 5: H 04 L 7/04
H 04 B 7/212

(22) Date of filing: 04.09.89

(30) Priority: 07.09.88 JP 222275/88

(71) Applicant: KOKUSAI DENSHIN DENWA CO., LTD
3-2, Nishi-shinjuku 2-Chome
Shinjuku-ku Tokyo (JP)(43) Date of publication of application:
14.03.90 Bulletin 90/11(72) Inventor: Shinonaga, Hideyuki
10-3 Kurihara 4-Chome
Niza-shi Saitama (JP)

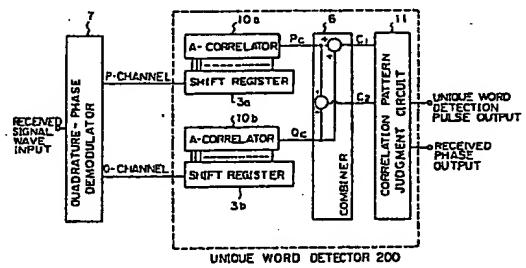
(84) Designated Contracting States: FR GB

(74) Representative: de Beaumont, Michel
1bis, rue Champollion
F-38000 Grenoble (FR)

(54) A unique word detection system.

(55) A unique word and/or a receive phase in the demodulator in a multi-phase signal and/or a multi-level signal is detected by obtaining a plurality of correlation values, performing linear calculations for those correlation values to provide a correlation pattern, and looking at a table if this pattern coincides with one of the predetermined patterns. The invention is useful even when a received signal quality is poor and/or the length of the unique word is short. A plurality of unique word, together with received phase are also recognized by the present invention.

Fig. 1A



EP 0 358 582 A2

Description**A Unique Word Detection System****BACKGROUND OF THE INVENTION**

5 The present invention relates to a unique word detecting system in a digital communication system used in a multiphase and/or multilevel modulation system.
 10 A multiphase/multilevel modulation system, such as a quadrature phase shift keying system, an eight-phase phase shift keying system, a sixteen valued QAM (Quadrature Amplitude Modulation) system or a sixty four-valued QAM system, is used in order to effectively utilize a radio frequency band in a digital communication system. A unique word is widely employed for the purpose of acquisition and maintenance of synchronization, phase ambiguity removal generated by a demodulator, and a signal type recognition and the like in such a digital communication system. In the multiphase or multilevel system, information bits with more than two bits are transmitted in parallel at the same time by one transmission symbol. Accordingly, more than two bits of the unique word are also transmitted in parallel at the same time by one transmission symbol. When
 15 the information bits are transmitted in parallel by N bits per transmission symbol, the number of the bits of the unique word transmitted in parallel is not always N and sometimes is less than N depending on the multiphase/multilevel modulation system. For example, in the case of the quadrature phase shift keying system one transmission symbol transmits two information bits and also two bits of the unique word are transmitted in parallel at the same time by one transmission symbol. On the other hand, in the case of the
 20 eight-phase phase shift keying system, while one transmission symbol transmits three information bits, two bits of the unique word are generally transmitted in parallel.
 25 Fig.5 shows the structure of a prior unique word detector employed in a multiphase/multilevel modulation system.

25 In Fig.5, a reference numeral 1 designates a multiphase or multilevel demodulator, 2 is a parallel/series converter for converting parallel data sequences to a series data sequence, 3 is a shift register, 4 is a correlator and 5 is a threshold judgment circuit. The operation of the unique word detector 100 shown in Fig.5 is briefly described below. Received signal is inputted to the multiphase/multilevel demodulator 1 and information bits with N bits per transmission symbol are outputted in parallel. When M bits ($M < N$) of a unique word are transmitted with one transmission symbol in parallel, these M bits are inputted to the parallel/series converter 2 to be converted to a series data sequence. At this time, the clock rate is also multiplied by M. Thereafter, the parallel/series-converted data sequence is inputted to the shift register 3. The shift register 3 is inputted with a new piece of data at every clock time and shifts the old data to the right bit by bit, resulting in the deletion of the oldest data. The whole data in the shift register 3 which have the same length as the unique word length are inputted to the correlator 4 in parallel at every clock time and a correlation value with respect to the unique word pattern is computed. When the data in the shift register 3 are $a_0, a_1 \dots a_{N-1}$ and the unique word patterns are $u_0, u_1 \dots u_{N-1}$, the correlation value R is computed by
 30
 35

$$40 \quad R = \sum_{i=0}^{N-1} a_i \oplus u_i$$

40 where a_i and u_i ($i=0, 1 \dots N-1$) take values 0 and 1, respectively. A symbol \oplus means an exclusive OR, taking the value 0 when a_i and u_i coincide with each other and 1 when do not. In short, the correlation value R is equal to the number of inconsistent bits in the data of the shift register 3 and the unique word pattern. Such correlation value is generally called the Hamming distance. Further, the difference between the number of consistent bits and the number of inconsistent bits in the data of the shift register 3 and the unique word pattern is often used as the correlation value. In this case, the correlation value takes either positive or negative value. A description will be made hereinafter by using the correlation value that takes the positive or negative value. A difference between the definitions of said two kinds of correlation value is expedient and has none of a relation with unique word detection characteristics.
 45

50 The unique word is detected by threshold-judging a correlation value produced at every clock time. In the unique word detector 100 described above and shown in Fig.5, however, the clock rate is increased by the parallel/series converter 2 and, as a result, processing in the correlator must be fairly rapidly performed.

55 In order to solve that problem, a unique word detector 101 whose structure is shown in Fig.6 is widely employed. In Fig.6, a reference numeral 1 designates a multiphase/multilevel demodulator, 3 (3a-3m) are shift registers, 4 (4a-4m) are correlators, 6 is a combiner and 5 is a threshold judgment circuit. Different from the unique word detector 100 shown in Fig.5, no parallel/series conversion is conducted, and parallel M bits are inputted to M numbers of the shift registers 3. In the case of Fig.6, when the whole unique word length is N bits, the length of each shift register 3 is N/M . The whole data in each shift register 3 are inputted to the corresponding correlator 4 at every clock time and the correlation value are computed therein. Thereafter, the correlation value for the whole unique word is computed by the combiner 6 and a unique word detection is performed through a threshold judgement by the threshold judgment circuit 5.
 60

For the detailed description of a prior unique word detecting system, the structure of a unique word

detector 102 for the quadrature phase shift keying system is illustrated in Fig.7. It is presumed that the unique word detector performs also the judgment of the received phase in the demodulator together with the detection of the unique word because phase ambiguity exists in the received phase of the quadrature-phase demodulator. In Fig.7, a reference numeral 7 designates a quadrature demodulator, 3 (3_a, 3_b) are shift registers, 8 (8_a, 8_b) are P-correlators, 9 (9_a, 9_b) are Q-correlators, 6 is a combiner and 5 is a threshold judgment circuit. The received quadrature phase shift keyed signal is inputted to the quadrature demodulator 7 to be demodulated therein. Two information bits are outputted in parallel from the quadrature-phase demodulator 7. Since two parallel output sequences are generally called a P-channel and a Q-channel, the two parallel output sequences as shown in Fig.7 will be called the P-channel and Q-channel hereinafter. P-channel and Q-channel data outputted from the quadrature-phase demodulator 7 are inputted to the shift registers 3_a and 3_b respectively. The whole data in the shift registers 3 are inputted to the P-correlators 8 and the Q-correlators 9 at every clock time. The P-correlators 8 and the Q-correlators 9 compute the correlation value with respect to the unique word pattern transmitted in the P-channel and Q-channel, respectively. Thereafter, four correlator outputs are inputted to the combiner 6. The combiner 6 computes the sum C₁ of the P-correlator output of the P-channel and the Q-correlator output of the Q-channel and a difference C₂ between the Q-correlator output of the P-channel and the P-correlator output of the Q-channel to give the two computed results C₁ and C₂ to the threshold judgment circuit 5. The threshold judgment circuit 5 detects the unique word when the absolute value of C₁ or C₂ exceeds the threshold value, and recognizes the received phase in the quadrature-phase demodulator 7 according to the sign of C₁ or C₂ which exceeds the threshold value.

The operation of the threshold judgment circuit 5 will be described in more detail below. The threshold judgment circuit 5 judges whether the absolute value of C₁ or C₂ exceeds a single threshold value. A judgement can also be possible by setting positive and negative threshold values for C₁ and C₂. When the absolute value of C₁ or C₂ is judged to exceed the threshold value, the received phase in the quadrature demodulator 7 is judged by which of C₁ and C₂ exceeds the threshold value, and by whether a polarity is positive or negative. Then a unique word detection pulse and a received phase judgment result are outputted. The threshold value is so set as to satisfy the specifications for a miss detection probability wherein the unique word can not be detected when the unique word exists and for a false detection probability wherein the unique word is detected in error when no unique word exists. Frequently, the unique word patterns transmitted by the P-channel and Q-channel are designed the same as each other or the P-channel pattern of the unique word is the inverted pattern of the Q-channel unique word bit by bit. In that case, the unique word detector 102 shown in Fig.7 can be simplified by omitting the Q-correlator 9 as shown in Fig.8.

Further, in some unique word detectors, not the correlation value resulted from subtracting the number of the inconsistent bits from the number of the consistent bits, but the number of inconsistent bits (the Hamming distance) or the number of the consistent bits is used in the correlator to detect the unique word. Even in such a case, the operation of the threshold judgment circuit 5 is equivalent. Therefore, unique word detection characteristics are quite the same.

The following problems, however, are present in the conventional systems described above:

The threshold value is so selected that the miss and false detection probabilities are less than the specified values. Generally, when the threshold value is made larger, the miss detection probability is decreased and, conversely, the false detection probability is increased. Accordingly, when the threshold value is T, the miss detection probability is larger than the specified value and the false detection probability is smaller than the corresponding specified value, and when the threshold value is T + ΔT (ΔT: the unit of an increase or a decrease in the threshold value), the miss detection probability is smaller than the specified value, and the false detection probability is larger than the specified value, it is determined that the unique word pattern can not satisfy the specification and the unique word with a longer sequence must be used. As a result, the circuit scales of the unique word detectors 100, 101, 102 or 103 must be enlarged and the transmission efficiency of the digital communication system is decreased.

Further, in the conventional systems, when the number of errors contained in the received unique word pattern becomes large the unique word can not be detected. For example, in the quadrature phase shift keying system described above, if the whole unique word length is N, and the error of N/4 bits occurs, the absolute values of the combiner outputs C₁ and C₂ is sometimes equal to each other and, in this case, the detection of the unique word itself is unable. In that case, the threshold value is designed to be smaller than N/4. When the miss detection probability is larger than the specified value, the unique word with further longer sequence must be used. As a result, the circuit scales of the unique word detectors 100, 101, 102 or 103 must be enlarged and the transmission efficiency of the digital communication is decreased.

SUMMARY OF THE INVENTION

It is an object, therefore, of the present invention to overcome the disadvantages and limitations of a prior unique word detection system by providing a new and improved unique word detection system.

It is also an object of the present invention to provide a unique word detection system in which it is possible to detect a unique word even in a poor transmission quality.

It is also an object of the present invention to provide a unique word detection system in which the length of a unique word may be short.

It is also an object of the present invention to provide a unique word detection system in which not only a unique word but also a received phase in the demodulator are detected.

It is still another object of the present Invention to provide a unique word detection system which detects a plurality of unique words.

The above and other objects are attained by a unique word detection system having; a plurality of correlation means for providing a plurality of correlation values between a plurality of output data outputted by a demodulator data and a predetermined unique word, a linear operation circuit for performing linear operations for said correlation values to provide a correlation pattern, and means for judging a unique word and the received phase in the demodulator by comparing said correlation pattern with the predetermined correlation patterns to recognize presence of a unique word in a receive signal when said correlation pattern coincides with one of said predetermined patterns.

10

BRIEF DESCRIPTION OF THE DRAWINGS

The foregoing and other objects, features, and attendant advantages of the present invention will be appreciated as the same become better understood by means of the following description and accompanying drawings wherein;

15

Fig.1A is a block diagram of the unique word detection system according to the present invention,
Fig.1B shows a table of a read only memory which implements a correlation pattern judgment circuit
(11) in Fig.1A,

Fig.2 shows the relationship among the number of correlation pattern groups to be assigned, and a miss detection probability and a false detection probability in the present invention,

20

Fig.3 shows a block diagram of another embodiment of the unique word detection system according to the present invention,

Figs.4(a) through 4(p) show two correlation values C_1 and C_2 when no error exists in a unique word, and

Fig.5, Fig.6, Fig.7 and Fig.8 are block diagrams of prior arts.

25

DESCRIPTION OF THE PREFERRED EMBODIMENTS

An embodiment 1 will be described for the digital communication system using the quadrature phase shift keying system. It is assumed that each unique word pattern transmitted by the P-and Q-channels has 011110001001

30

of a twelve bit sequence (hereinafter called "A-sequence"). Further, it is assumed that the received phase in the quadrature-phase demodulator is also judged.

35

Fig.1A shows the first embodiment according to the present invention, illustrating the structure of a unique word detector. In Fig.1A, the reference numerals 3_a and 3_b designate shift registers storing the data of the P-channel and the Q-channel, respectively, 7 is a quadrature-phase demodulator, 10_a and 10_b are A-correlators for storing said A-sequence and calculating the correlation value between said A-sequence and the data in the shift register 3, 6 is a combiner for combining (linear operations) the outputs of the A-correlators 10_a and 10_b , and 11 is a correlation pattern judgment circuit for detecting the unique word and the received phase in the demodulator 7 on the basis of the predetermined correlation patterns.

40

An operation will be described below. The received phase in the quadrature-phase demodulator 7 has four states which are represented by I, II, III and IV. That is, when transmitted P-channel and Q-channel data are (P, Q), respectively, the received P-channel and Q-channel data are one of the four states of I (P, Q), II (Q, \bar{P}), III (\bar{P} , Q) and IV (\bar{Q} , P), respectively.

The unique word pattern is inputted to the unique word detector in the form of one of four patterns (A, A), (A, \bar{A}), (\bar{A}, \bar{A}) and (\bar{A}, A) depending on the received phase in the demodulator 7.

45

The symbol P , Q , or A shows the inverted pattern of P , Q , or A . For instance, when $A=011110001001$, $\bar{A}=100001110110$.

50

A received quadrature-phase-modulated signal is inputted to the quadrature-phase demodulator 7 which demodulates the same. The P-channel and Q-channel data outputted from the quadrature-phase demodulator 7 are inputted to the shift registers 3, and 3_b , respectively. The length of each shift register 3 is equal to that of the A-sequence, twelve bits. The shift registers 3 are inputted with new data and shift the old data to the right at every clock time. The whole data in the shift registers 3 are outputted to the A-correlators 10_a and 10_b at every clock time. The A-correlators 10 compute the correlation values (P_c, Q_c) between the data (P, Q) inputted from the shift registers 3 and the A-sequence stored in the A-correlators 10. Correlation values (P_c, Q_c) from the two A-correlators 10 are inputted to the combiner 6, wherein the sum $C_1 (=P_c + Q_c)$ of and a difference $C_2 (=P_c - Q_c)$ between the A-correlator 10a output of the P-channel and the A-correlator 10b output of the Q-channel are computed. Thereafter, the sum C_1 and the difference C_2 are inputted to the correlation pattern judgment circuit 11, which is the feature of the present invention. The correlation pattern judgment circuit 11 detects the unique word and judges the received phase in the demodulator 7 on the basis of the pattern of the sum C_1 and the difference C_2 . That is, in the present invention, when the pattern of the sum C_1 and the difference C_2 coincides with one of the correlation patterns stored in advance, the unique word is recognized and the received phase in the demodulator 7 is judged. The correlation pattern judgment circuit 11, the feature of the present invention, will be described in detail below.

60

The received unique word generally includes errors due to a noise and the like in a transmission line. The correlation pattern (C_1, C_2) in a case with no error in the unique word is one of (24, 0), (0, 24), (-24, 0) and (0, -24), which correspond to the received phases I, II, III and IV, respectively.

Table 1

number of error bits	correlation pattern
0	(24, 0) (0, 24) (-24, 0) (0, -24)
1	(22, 2) (2, 22) (-22, -2) (-2, -22)
2	(20, 0) (0, 20) (-20, 0) (0, -20)
3	(18, 2) (2, 18) (-18, -2) (-2, -18)
4	(16, 0) (0, 16) (-16, 0) (0, -16)
5	(14, 2) (2, 14) (-14, -2) (-2, -14)
6	(12, 0) (0, 12) (-12, 0) (0, -12)

When errors are included in the unique word, the correlation pattern changes. Table 1 shows the correlation patterns (C_1 , C_2) when error bits equal to or less than 6 are included in the unique word.

In Table 1, the correlation pattern groups for each number of errors have four rows, and the first, second, third and fourth rows correspond to the correlation patterns $(24, 0)$, $(0, 24)$, $(-24, 0)$ and $(0, -24)$, respectively,

when errors are not contained. As is obvious from Table 1, while the four kinds of the correlation patterns (24, 0), (0, 24), (-24, 0) and (0, -24) have a relationship that C_1 and C_2 are interchanged and/or a sign is inverted. When errors are included, the same relationship as above is maintained among the correlation patterns. In short, when the correlation pattern (a, b) exists, correlation patterns (b, a), (-a, -b) and (-b, -a) also exist.

5 No same correlation pattern exists when the number of the error bits is equal to or smaller than five. However, each of the correlation patterns (12, 12), (12, -12), (-12, -12) and (-12, 12) is appeared twice when the number of the error bits is six as enclosed by the dotted line in the Table 1. Therefore, when one of four kinds of correlation patterns in said dotted line area is detected, it is impossible to determine which one of the four basic correlation patterns (24, 0), (0, 24), (-24, 0) and (0, -24) the received pattern corresponds to, and therefore the received phase can't be determined. The reason why the unique word including six error bits can't be detected in the conventional unique word detecting systems is that the absolute values of C_1 and C_2 becomes equal to each other as described above on rare occasions.

10 15 In the unique word detection system according to the present invention, the correlation patterns from which the correlation pattern with no error can not be solely determined are removed and appropriate number of correlation pattern groups are selected out of the remaining, and the correlation pattern (C_1 , C_2) is compared with the selected (or assigned) pattern groups to see if the same pattern exists.

That is, the correlation pattern judgment circuit 11 judges that the unique word is detected when one of the assigned correlation patterns is detected and the received phase is judged which one of the basic patterns (24, 0), (0, 24), (-24, 0) and (0, -24) the detected correlation pattern corresponds to.

20 It should be noted in the Table 1 that even if the number of error bits is six, the probability that the pattern is the same as that enclosed by the dotted line is rather small. And, when the pattern does not coincide with that in the dotted line, the unique word is detected even when there are six error bits.

The correlation pattern judgment circuit 11 may be implemented by a memory table. That memory table may be a read only memory.

25 Fig.1B shows an example of that table, in which the horizontal line shows the address C_1 (24,22,20,..., -22,-24), and the vertical line shows the address C_2 (24,22,20,..., -22,-24). It should be noted that only the even number addresses for C_1 and C_2 are enough, since those numbers take only even number in the embodiment.

In the Table 1, the pattern (24,0) means the received phase I. Therefore, in the table in Fig.1B, the number 1 is stored in the address ($C_1=24$, $C_2=0$). Similarly, in the table 1 in Fig.1B, the address ($C_1=0$, $C_2=24$) stores 2, the address (-24,0) stores 3, and the address ($C_1=0$, $C_2=-24$) stores 4. Similarly, the address ($C_1=22$, $C_2=2$) stores 1, ($C_1=22$, $C_2=-2$) stores 1, ($C_1=2$, $C_2=22$) stores 2, ($C_1=-22$, $C_2=-2$) stores 3, and ($C_1=-2$, $C_2=-22$) stores 4. All the addresses which are not defined by the Table 1 stores 0 in the table shown in Fig.1B.

30 35 In the above structure of the correlation pattern judgment circuit, when the pattern (C_1 , C_2) is applied to the table of Fig.1B as the address of the same, the table provides 0, 1, 2, 3 or 4 as an output signal. When the output signal of the table is 0, it means that no unique word is detected. On the other hand, when the output signal is 1, 2, 3 or 4, it means that the unique word is detected, and the received phase is given by the output of the table.

It should be noted of course that the addresses ($C_1=12$, $C_2=12$), ($C_1=12$, $C_2=-12$) et al in the memory table store of course 0.

40 It should be appreciated in the above explanation that the feature of the present invention is the use of the comparison of the correlation pattern (C_1 , C_2) with predetermined correlation patterns to detect the presence of the unique word, together with the received phase. A prior art does not compare the pattern, but tests if the absolute value of the correlation values C_1 or C_2 exceeds the threshold value, and determines the received phase according to the sign and the type of the correlation value C_1 or C_2 which exceeds the threshold value.

45 Fig.2 is a characteristic diagram showing relationship among the number of predetermined correlation pattern groups to be assigned, and the miss detection probability and the false detection probability, together with the same relationship as above wherein the conventional threshold values are employed.

As is obvious from Fig.2, the miss detection probability and the false detection probability can be flexibly adjusted by increasing or decreasing the number of predetermined correlation pattern groups in the Table 1. 50 On the other hand, in the conventional system, when the threshold value is increased or decreased by one, the miss detection probability and the false detection probability change largely and a fine adjustment is difficult.

For example, in the conventional system in Fig.2, when the specification for the miss detection probability and the false detection probability takes the value A, the threshold value 5 can satisfy the specification. However, when the specification takes the value B, the miss detection probability can not satisfy the specification, if the threshold value is 5, and therefore, the unique word of the longer sequence must be used in order to meet the specified value B.

On the other hand, in the unique word detector 200 according to the present invention, the specification can be sufficiently satisfied when 26 correlation pattern groups are assigned. It should be appreciated in the Table 1 that the number of the correlation pattern groups is 26, which excludes those in the dotted line. The 60 number of correlation pattern groups for the errors 0, 1, 2, 3, 4, 5 and 6 are 1, 2, 3, 4, 5, 6, and 5. And, $1+2+3+4+5+6+5=26$.

As described above, according to the present invention, the miss detection probability and the false detection probability can be finely controlled as compared with the conventional system and the unique word including many errors unable to be detected by the conventional system can be detected.

65 Even when the correlation value is detected by detecting the number of the inconsistent bits (the Hamming

distance) or of the consistent bits, only the values of the correlation patterns are different and the present invention is applicable in the same manner. The unique word detection characteristics in that case are of course the same as that of Fig.2. Further, it can be seen that the unique word detection system according to the present invention can be implemented only by replacing the conventional threshold judgment circuit 5 with the correlation pattern judgment circuit 11. Therefore, an increase in the circuit scale due to the use of the unique word detecting system according to the present invention is small.

The embodiment 1 is the fundamental detection system according to the present invention, and the type of the unique words is not judged but the received phase in the demodulator 7 is judged. The unique word detecting system wherein both the type of the unique word and the received phase are judged will be described below.

(EMBODIMENT 2)

The unique word detection system according to the present invention will be described below by exemplifying the unique words employed in an INTELSAT TDMA (Time Division Multiplexing Access) system using the quadrature phase shift keying system.

In the INTELSAT TDMA system, four types of second unique words UW₀, UW₁, UW₂ and UW₃ mentioned below are employed.

The second unique word consists of two first unique words (for example, P-channel A and Q-channel A, P-channel A and Q-channel \bar{A} et al) which are 24 bits partial patterns of first unique words.

UW ₀	P channel AA
	Q channel AA
UW ₁	P channel $\bar{A}\bar{A}$
	Q channel AA
UW ₂	P channel AA
	Q channel $\bar{A}\bar{A}$
UW ₃	P channel $\bar{A}\bar{A}$
	Q channel AA

where A represents a 12 bit sequence 011110001001 as the same as in the embodiment 1 and \bar{A} represents a pattern wherein 1 and 0 are inverted in the A-sequence ($\bar{A}=100001110110$). Thus, each unique word is constituted by employing four A or \bar{A} -sequences. The detection of the unique word is performed such that first 24 bits partial patterns and second 24 bits partial patterns are detected separately and, only when both the parts are detected, the unique word is judged to be detected. Since four kind of the received phases (I, II, III, IV) exist as described with reference to the embodiment 1, four types of the unique words UW₀-UW₃, the feature of the embodiment 2, totally have sixteen unique word patterns mentioned below. The unique word pattern is named by the type of the unique word and the received phase. Preceding the unique word, a bit timing recovery sequence (hereinafter called "BTR") with a 0101...01 pattern (alternate 1 and 0 pattern) is added in the INTELSAT TDMA system. The bit timing recovery sequence affecting the correlation value is represented by the BTR which is included in the sixteen unique word patterns. The symbol $\overline{\text{BTR}}$ designate a pattern wherein 0 and 1 are inverted in the BTR sequence. There are the following sixteen kinds of the unique word patterns:

- (1) UW₀, I P-channel BTR A A
 Q-channel BTR A A
- (2) UW₀, II P-channel BTR A A
 Q-channel $\overline{\text{BTR}}$ \bar{A} \bar{A}
- (3) UW₀, III P-channel $\overline{\text{BTR}}$ \bar{A} \bar{A}
 Q-channel $\overline{\text{BTR}}$ \bar{A} \bar{A}
- (4) UW₀, IV P-channel $\overline{\text{BTR}}$ \bar{A} \bar{A}
 Q-channel BTR A A

	(5)	UW_1 , I	P-channel BTR	A	\bar{A}
			Q-channel BTR	A	A
5	(6)	UW_1 , II	P-channel BTR	A	A
			Q-channel $\overline{\text{BTR}}$	\bar{A}	A
10	(7)	UW_1 , III	P-channel $\overline{\text{BTR}}$	\bar{A}	A
			Q-channel $\overline{\text{BTR}}$	\bar{A}	\bar{A}
15	(8)	UW_1 , IV	P-channel $\overline{\text{BTR}}$	\bar{A}	\bar{A}
			Q-channel BTR	A	\bar{A}
20	(9)	UW_2 , I	P-channel BTR	A	A
			Q-channel BTR	A	\bar{A}
25	(10)	UW_2 , II	P-channel BTR	A	\bar{A}
			Q-channel $\overline{\text{BTR}}$	\bar{A}	\bar{A}
30	(11)	UW_2 , III	P-channel $\overline{\text{BTR}}$	\bar{A}	\bar{A}
			Q-channel $\overline{\text{BTR}}$	\bar{A}	A
35	(12)	UW_2 , IV	P-channel $\overline{\text{BTR}}$	\bar{A}	A
			Q-channel BTR	A	A
40	(13)	UW_3 , I	P-channel BTR	A	\bar{A}
			Q-channel BTR	A	\bar{A}
45	(14)	UW_3 , II	P-channel BTR	A	\bar{A}
			Q-channel $\overline{\text{BTR}}$	\bar{A}	A
50	(15)	UW_3 , III	P-channel $\overline{\text{BTR}}$	\bar{A}	A
			Q-channel $\overline{\text{BTR}}$	\bar{A}	A
55	(16)	UW_3 , IV	P-channel $\overline{\text{BTR}}$	\bar{A}	A
			Q-channel BTR	A	\bar{A}

55 The unique word detector finally detects one of above sixteen unique word patterns.

Fig.3 shows the structure of a unique word detector 201, a second embodiment according to the present invention. In Fig.3, a difference from the embodiment 1 (shown in Fig.1) is the presence of a unique word type and received phase judgment circuit 12 for judging above-mentioned sixteen unique word patterns by the present output and the output twelve clock time before of the correlation pattern judgment circuit 11. An operation will be described below for the difference from the embodiment 1.

60 The correlation pattern judgment circuit 11, as with the case of the embodiment 1, detects the correlation pattern and judges to which of (24, 0), (0, 24), (-24, 0) and (0, -24) the detected correlation pattern corresponds, if there were no error. The judged result is fed to the unique word type and received phase judgment circuit 12. The unique word type and received phase judgment circuit 12 refers to the judged result

of twelve clock time before. When the correlation pattern corresponding to any of (24, 0), (0, 24), (-24, 0) and (0, -24) was also detected twelve clock time before, the type of the unique word and the received phase are judged by Table 2 shown later and a unique word detection pulse and a received phase judgment result are outputted.

The reason why the judged result of twelve clock time before is referred to will be described below. In Figs.4, (a)-(p), C₁ and C₂ for the sixteen unique word patterns when no error exists are shown. The names of the unique word patterns are shown on the left and upper portions of figures in Figs.4. Further, C₁ and C₂ are shown in upper and lower graphic charts, respectively. An abscissa represents a displacement (in clock time) and the displacement 0 represents the time point when the unique word is to be detected correctly. A computation is conducted while presuming that the unique word is followed by the P-channel data 010101111100 and the Q-channel data 010001101101. It is seen from Figs.4 that the absolute value of C₁ or C₂ reaches 24 at the displacement -12 and 0. When Figs.4, (a)-(p) are compared with one another, it is realized that the sixteen unique word patterns can be completely identified by recognizing which of C₁ and C₂ the absolute value of the correlation value reaches 24 at the displacement -12 and 0 and, whether a polarity is positive or negative. Table 2 shows above-described relationship. In other words, the sixteen unique word patterns are encoded into sixteen types at the displacement -12 and 0.

When the absolute value of C₁ or C₂ exceeds the threshold value at two clock time points whose displacement is different by twelve, the unique word and the received phase were judged in the conventional unique word detecting system. On the other hand, the unique word and the received phase are judged on the basis of the correlation patterns of both C₁ and C₂ at the two clock time points whose displacement is different by twelve in the present invention.

The correlation patterns of C₁ and C₂ are the same as in afore-mentioned Table 1 if the number of error bits contained in the partial 24 bits pattern of the unique word is 0-6.

As described above, the detection of the type of the unique word can also be performed in addition to the judgment of the received phase in the embodiment 2.

A comparison between the effect of the present invention and a prior art will be described below.

The miss detection probability and false detection probability when the correlation patterns except those in an undetectable region encircled by the broken lines in Table 1 are assigned are as follows: The false detection probability is defined as the probability that the unique word is detected in error in an aperture with a width of 65 bits. A bit error rate is assumed to be 5×10^{-3} .

Table 2

unique word pattern	displacement -12 (C ₁ , C ₂)	displacement 0 (C ₁ , C ₂)
UW ₀ , I	(24, 0)	(24, 0)
UW ₀ , II	(0, 24)	(0, 24)
UW ₀ , III	(-24, 0)	(-24, 0)
UW ₀ , IV	(0, -24)	(0, -24)
UW ₁ , I	(24, 0)	(0, -24)
UW ₁ , II	(0, 24)	(24, 0)
UW ₁ , III	(-24, 0)	(0, 24)
UW ₁ , IV	(0, -24)	(-24, 0)
UW ₂ , I	(24, 0)	(0, 24)
UW ₂ , II	(0, 24)	(-24, 0)
UW ₂ , III	(-24, 0)	(0, -24)
UW ₂ , IV	(0, -24)	(24, 0)
UW ₃ , I	(24, 0)	(-24, 0)
UW ₃ , II	(0, 24)	(0, -24)
UW ₃ , III	(-24, 0)	(24, 0)
UW ₃ , IV	(0, -24)	(0, 24)

o Miss Detection Probability

The miss detection probability takes the common value 1.03×10^{-10} for the sixteen unique word patterns.

o False Detection Probability

The false detection probability takes the following three kinds of values due to a difference in the forms of the correlation values C₁ and C₂ shown in Figs.4:

2.0443 x 10 ⁻¹⁰	UW ₀ -UW ₆ , IV
5.2953 x 10 ⁻¹¹	UW ₁ , I-UW ₁ , IV
	UW ₂ , I-UW ₂ , IV
5.3005 x 10 ⁻¹¹	UW ₃ , I-UW ₃ , IV

5

Further, the miss detection probability and the false detection probability when the conventional unique word detecting system is employed are as follows: In this case, the threshold value is assumed to be 5.

10 o Miss Detection Probability

The miss detection probability takes the common value 3.89×10^{-9} for the sixteen unique patterns.

o False Detection Probability

15

2.3717 x 10 ⁻¹³	UW ₀ , I-UW ₀ , IV
2.2804 x 10 ⁻¹³	UW ₁ , I-UW ₁ , IV
	UW ₂ , I-UW ₂ , IV
2.2805 x 10 ⁻¹³	UW ₃ , I-UW ₃ , IV

20

Thus, when the specified value for the miss detection probability and the false detection probability is 1×10^{-8} , the specification can be satisfied even by the conventional unique word detection system. However, when the specified value is 1×10^{-9} , the miss detection probability can no longer satisfy the specification by the conventional unique word detection system and the unique word with the longer sequence must be employed. The unique word detection system according to the present invention, however, sufficiently satisfies even the specified value of 1×10^{-9} .

It has become clear that all the correlation patterns except those in the encircled ones by the dotted lines in Table 1 makes an optimum case wherein the miss detection probability and the false detection probability are kept less than the specified value and the sum of both the probabilities are minimized. In this case, the sum of both the probabilities is reduced to less than one-tenth as compared with a conventional case and a communication quality becomes better to that extent. Thus, the present invention provides an optimum unique word detection.

While the quadrature-phase modulation is exemplified in the description above, it is a matter of course that the present invention can be similarly applied to the system employing other unique words or other systems adopting other multiphase/multilevel modulation systems, such as the eight-phase phase shift keying system, a sixteen-valued QAM system or a sixty-four-valued QAM system. While the embodiments employing the shift registers and the digital correlators to obtain the correlation values are described above, the employment of an analog correlator or the like has no objection.

As described above, since the unique word is detected on the basis of the correlation patterns in the present invention, the fine control can be effected for the miss detection probability and the false detection probability. Further, since the unique word including the errors in the number that has been unable to be detected in the conventional system can be detected in part, the miss detection probability can be reduced. As a result, even when the specified value is not satisfied to a slight degree and the unique word of the longer sequence must be employed in the conventional system, the specified value may be satisfied according to the present invention in many cases. Thus, the circuit scale of the unique word detector can be reduced and the transmission efficiency of the digital communication system can be improved.

Further, it is even possible that the sum of the miss detection and false detection probabilities are minimized while keeping both the probabilities less than the specified value to perform the optimum unique word detection according to the present invention, resulting in an improvement in the quality of the communication system.

Still further, by comparing the current unique word and the unique word of twelve clock time before with the predetermined correlation patterns, the presence or absence and the type of the unique word and the received phase in the demodulator can be detected at the same time.

Therefore, the unique word detection system according to the present invention are widely suitable for the digital communication system in general, such as the TDMA system, a TDM (Time Division Multiplex) system, a maritime, aeronautical satellite communication system and a subscriber radio system. The effect of the unique word detection system according to the present invention is large.

From the foregoing it will now be apparent that a new and improved unique word detection system has been found. It should be understood of course that the embodiments disclosed are merely illustrative and are not intended to limit the scope of the invention. Reference should be made to the appended claims, therefore, rather than the specification as indicating the scope of the invention.

65

Claims

(1) A unique word detection system comprising:
 a plurality of correlation means for providing a plurality of correlation values between a plurality of output data demodulated by a demodulator and one or more predetermined patterns,
 a linear operation circuit for performing linear operation for said correlation values to provide a correlation pattern, and
 means for judging a unique word by comparing said correlation pattern with predetermined correlation patterns to recognize presence of a unique word in a received signal when said correlation pattern coincides with one of said predetermined patterns.

(2) A unique word detection system according to claim 1, wherein said means for judging a unique word determines both presence of a unique word and received phase in the demodulator by comparing correlation pattern with said predetermined patterns.

(3) A unique word detection system for a digital communication system wherein a plurality of first unique words, which form a second unique word, are employed comprising;
 a plurality of correlation means for providing a plurality of correlation values between a plurality of output data demodulated by a demodulator and one or more predetermined patterns,
 a linear operation circuit for performing linear operation for said correlation values to provide a correlation pattern,
 means for judging a first unique word by comparing said correlation pattern with predetermined correlation patterns to recognize presence of a first unique word in a received signal when said correlation pattern coincides with one of said predetermined patterns, and
 means for judging a second unique word coupled with output of said means for judging a first unique word to recognize presence of a second unique word in a received signal based upon a plurality of outputs of said means for judging a first unique word.

(4) A unique word detection system according to claim 3, wherein said means for judging a second unique word determines both presence of a second unique word and received phase in the demodulator based upon a plurality of outputs of said means for judging a first unique word.

(5) A unique word detection system according to claim 3, wherein said means for judging a second unique word determines presence and type of the second unique word based upon a plurality of outputs of said means for judging a first unique word.

(6) A unique word detection system according to claim 3, wherein said means for judging a second unique word determines presence and type of the second unique word and received phase in the demodulator based upon a plurality of outputs of said means for judging a first unique word.

(7) A unique word detection system according to claim 1, wherein said means for judging unique word is implemented by a read only memory table.

(8) A unique word detection system according to claim 3, wherein said means for judging unique word is implemented by a read only memory table.

(9) A unique word detection system according to claim 1, wherein said correlation pattern is one of (A,B), (B,A), (-A,-B), and (-B,-A).

(10) A unique word detection system according to claim 3, wherein said correlation pattern is one of (A,B), (B,A), (-A,-B), and (-B,-A).

5

10

15

20

25

30

35

40

45

50

55

60

65

Fig. 1A

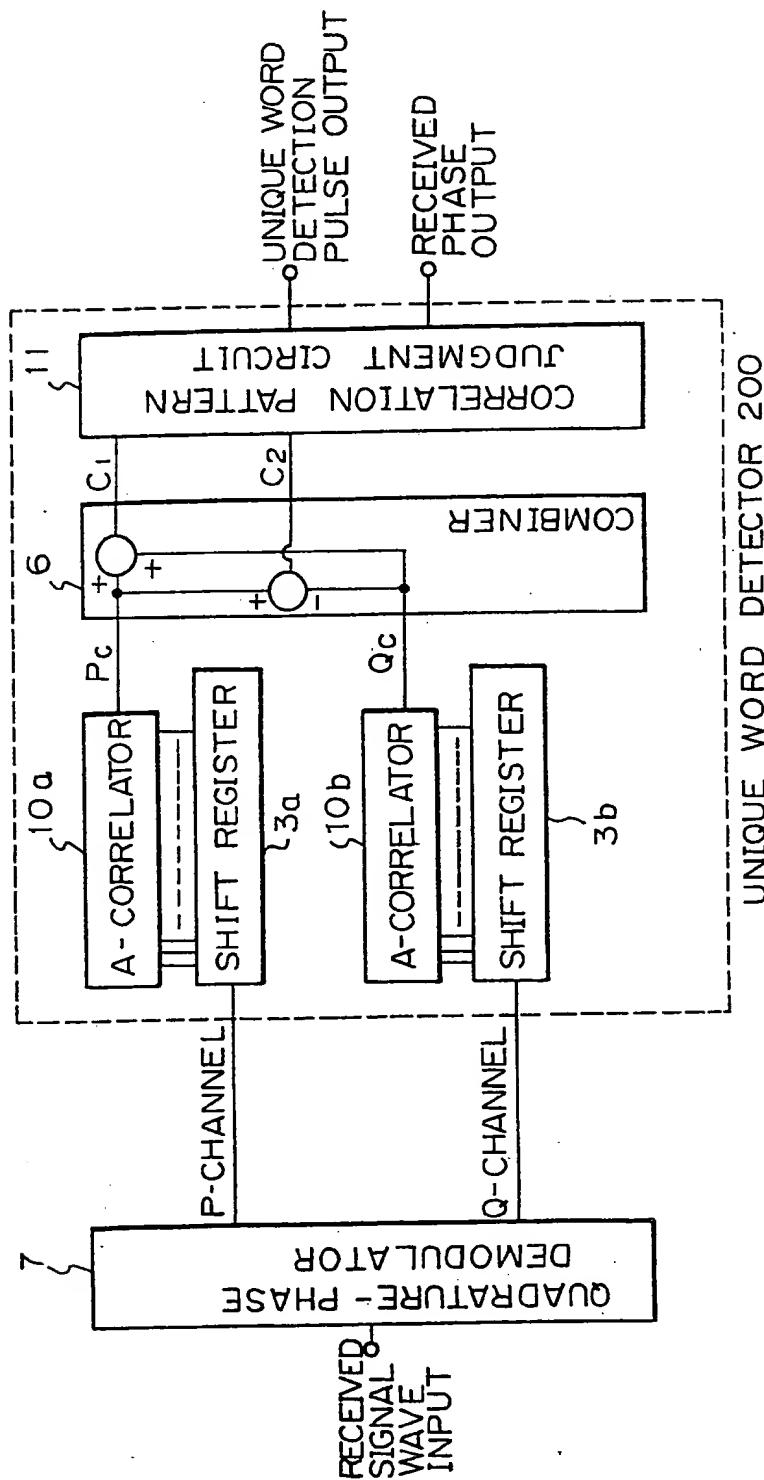
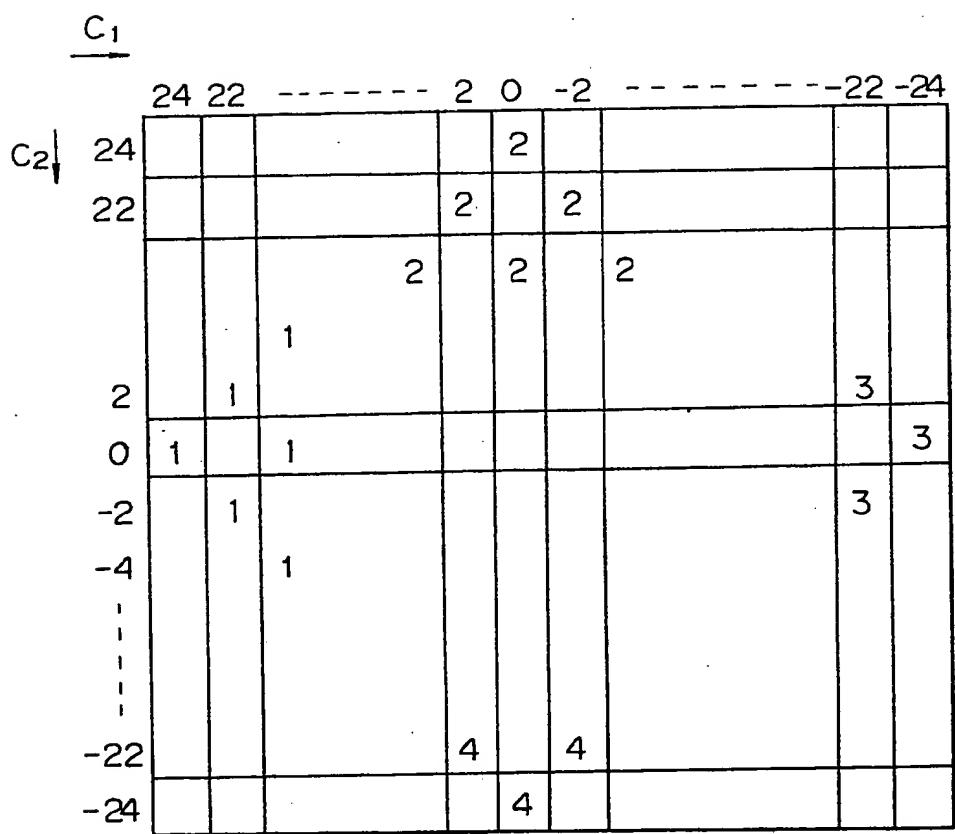


Fig. 1B



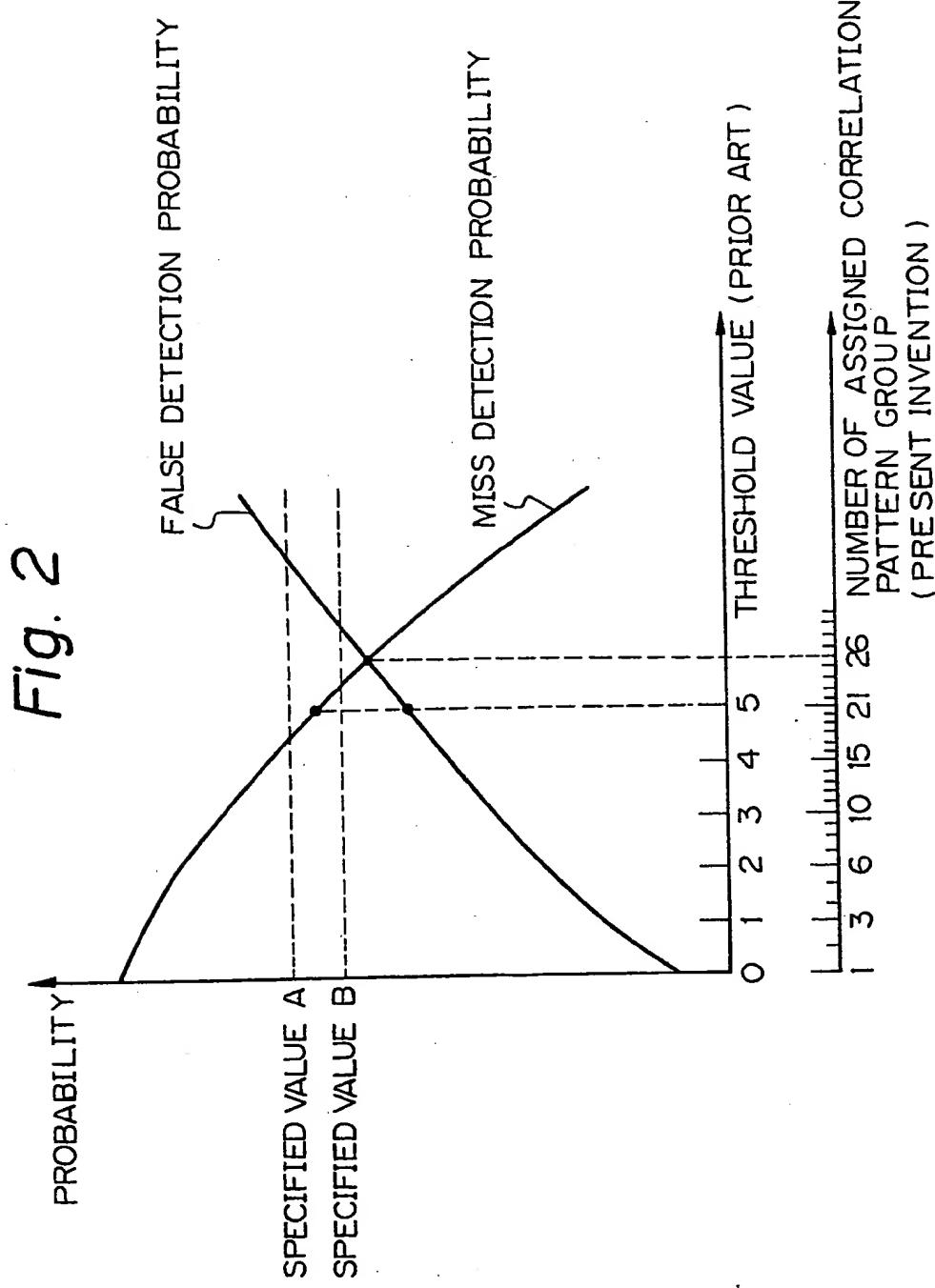


Fig. 3

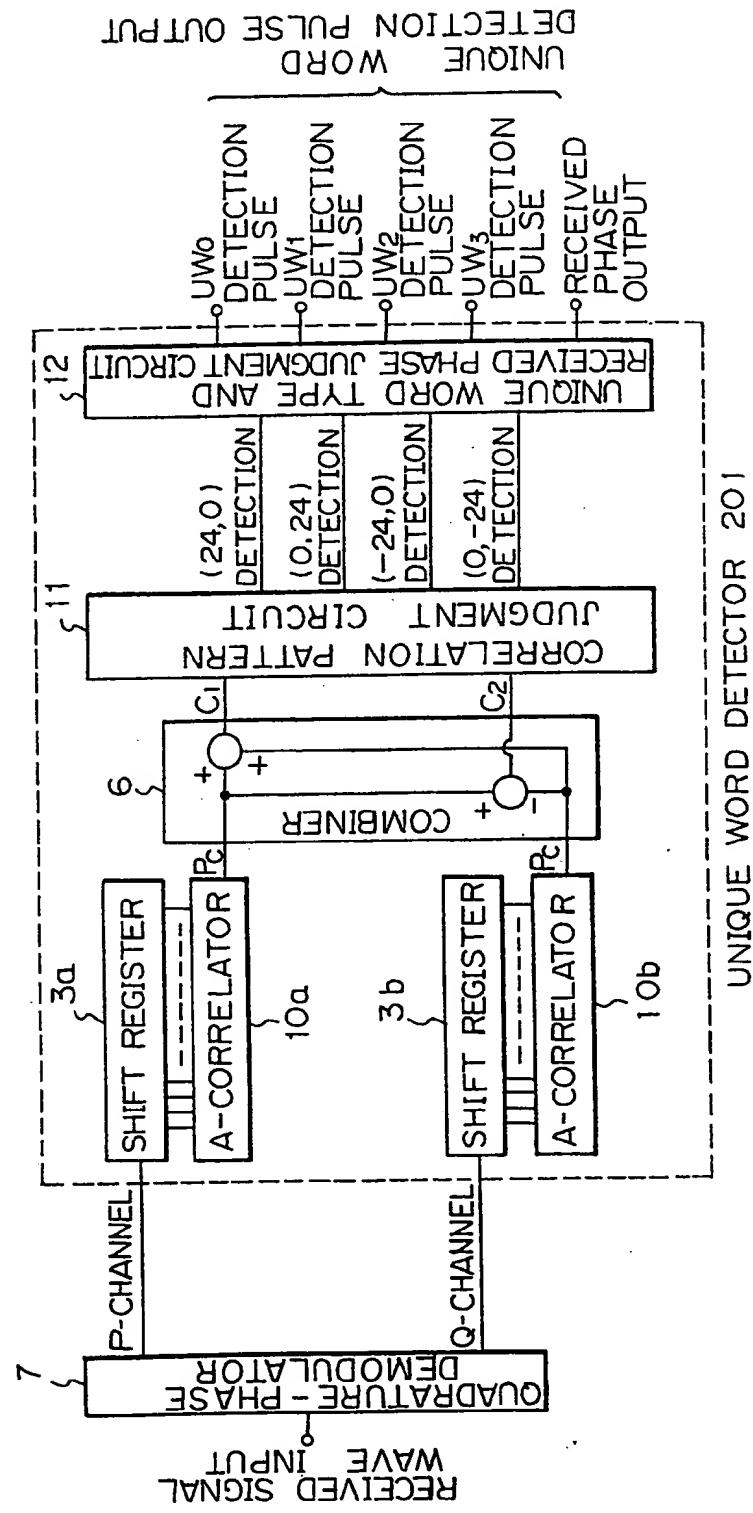


Fig. 4(a)

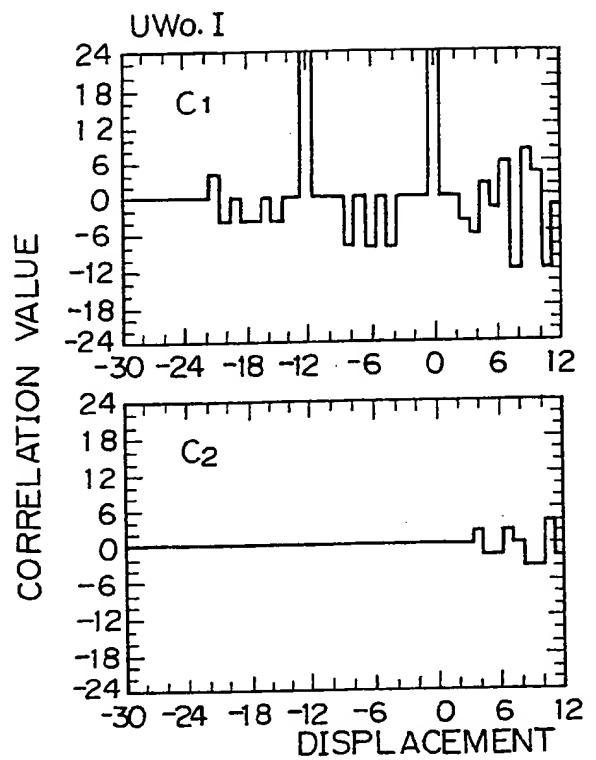


Fig. 4(b)

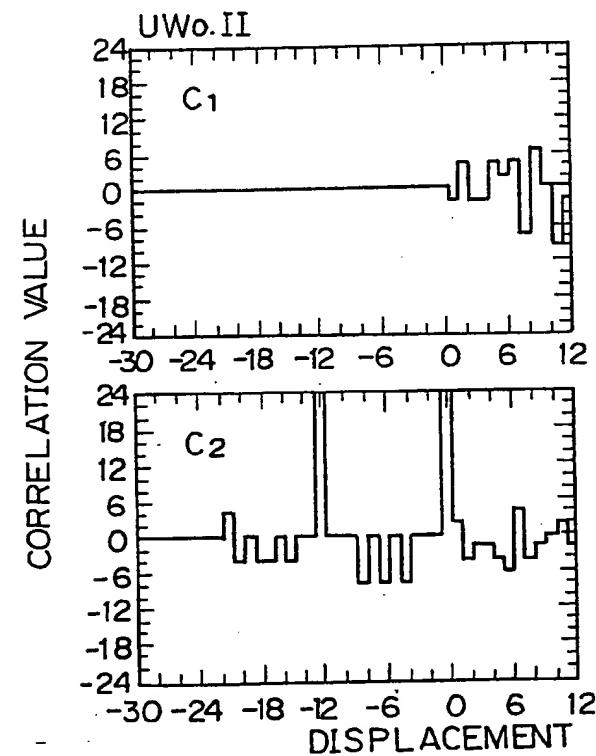


Fig. 4(c)

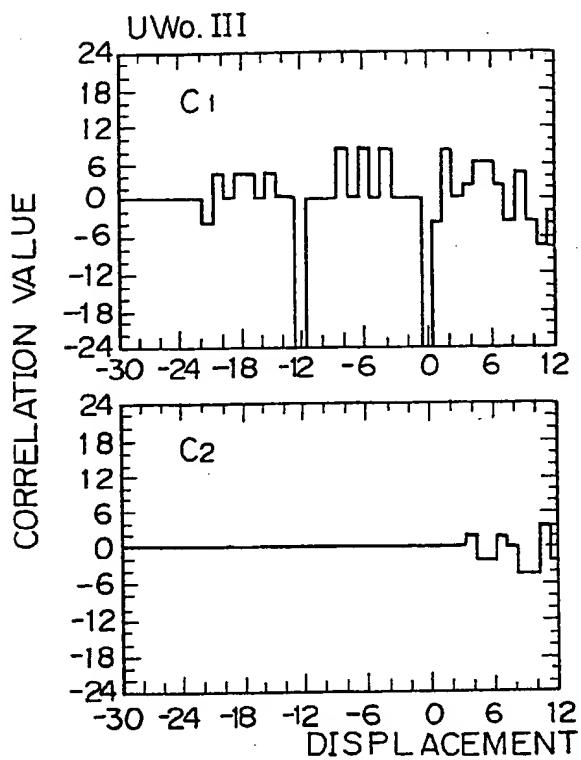


Fig. 4(d)

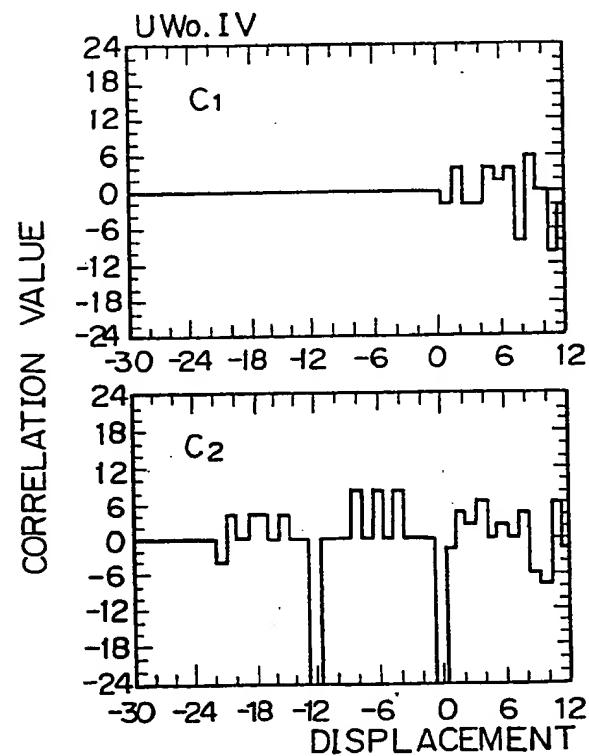


Fig. 4(e)

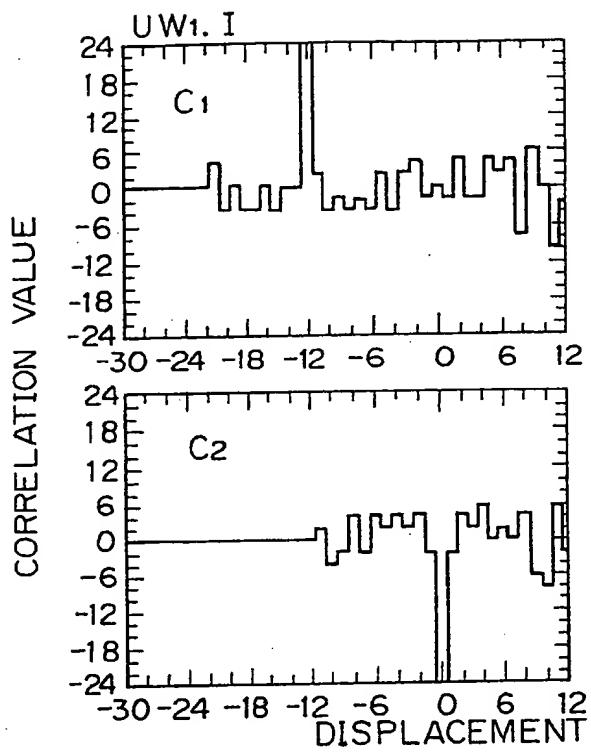


Fig. 4(f)

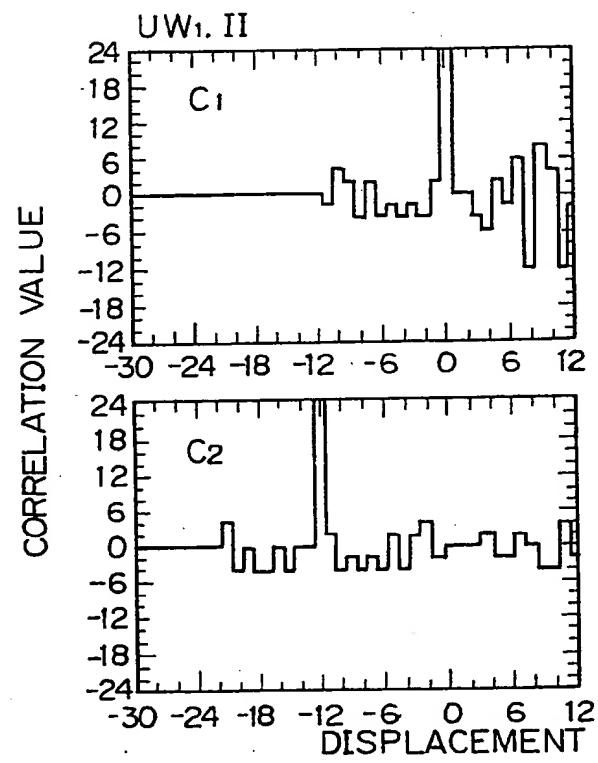


Fig. 4(g)

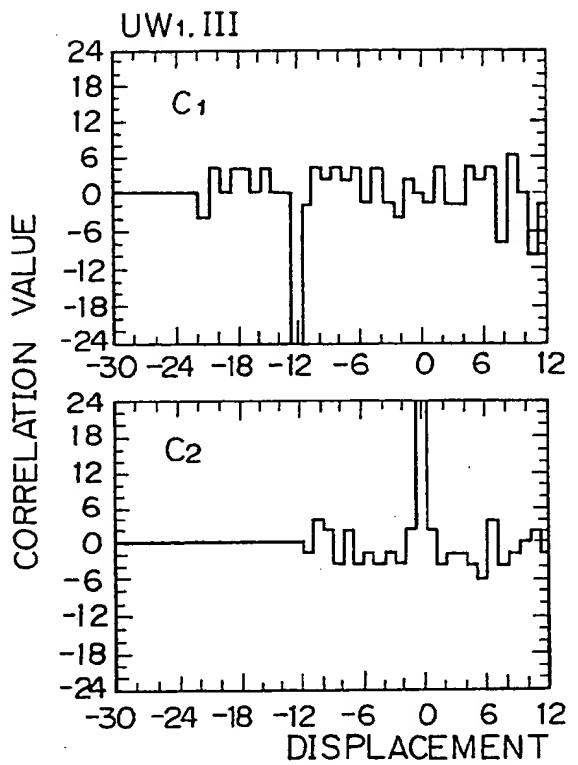


Fig. 4(h)

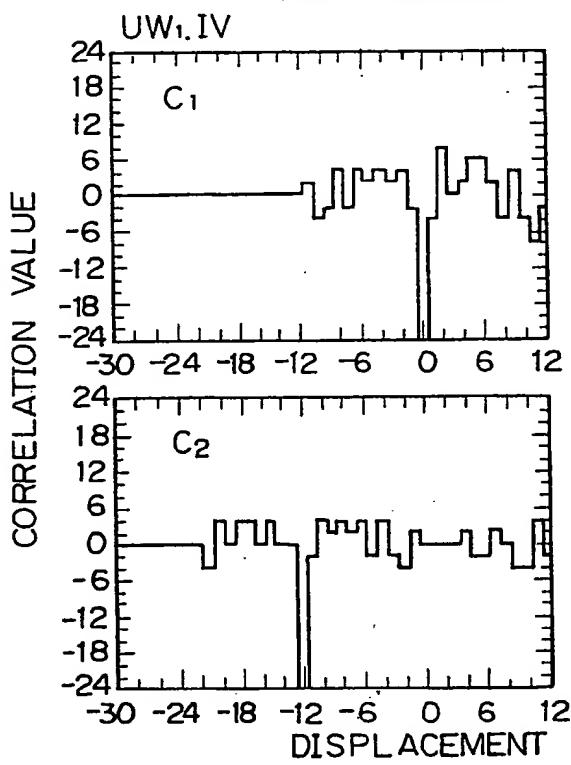


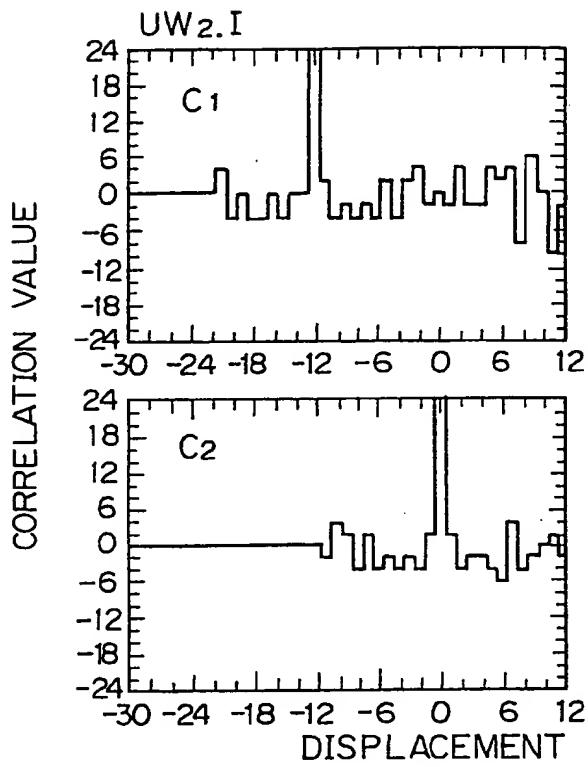
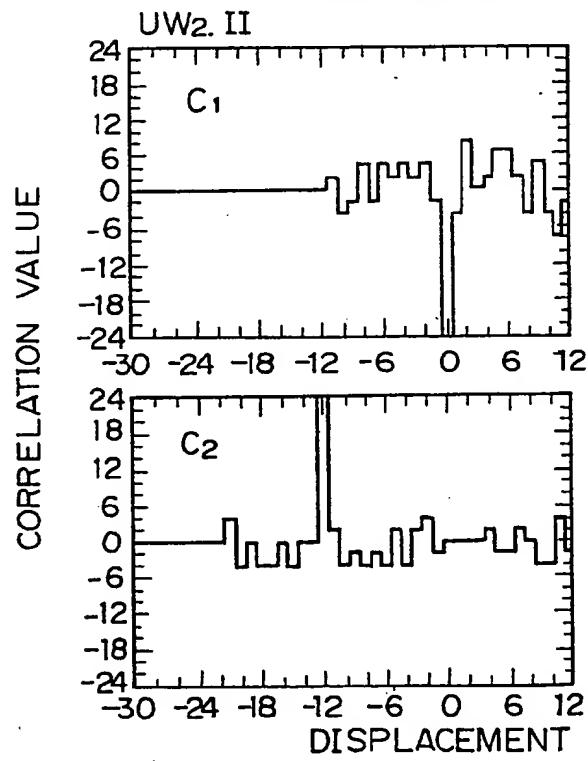
Fig. 4(i)*Fig. 4(j)*

Fig. 4(k)

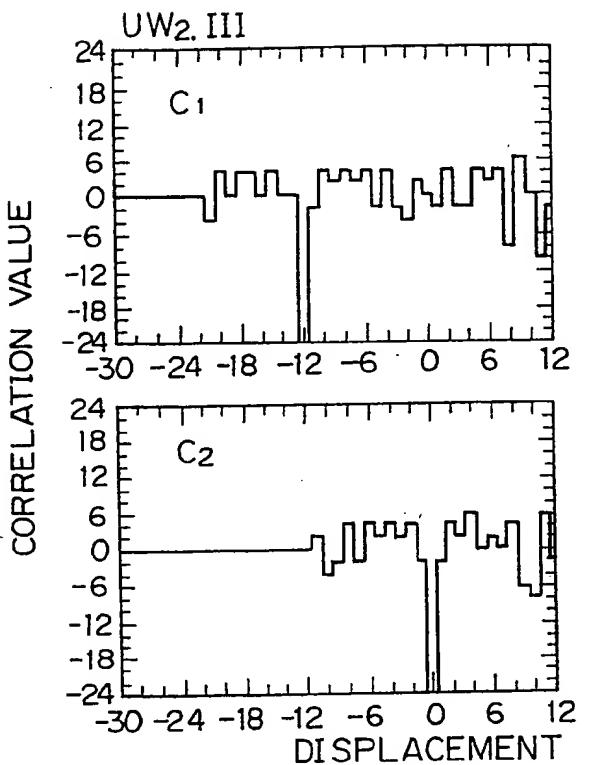


Fig. 4(l)

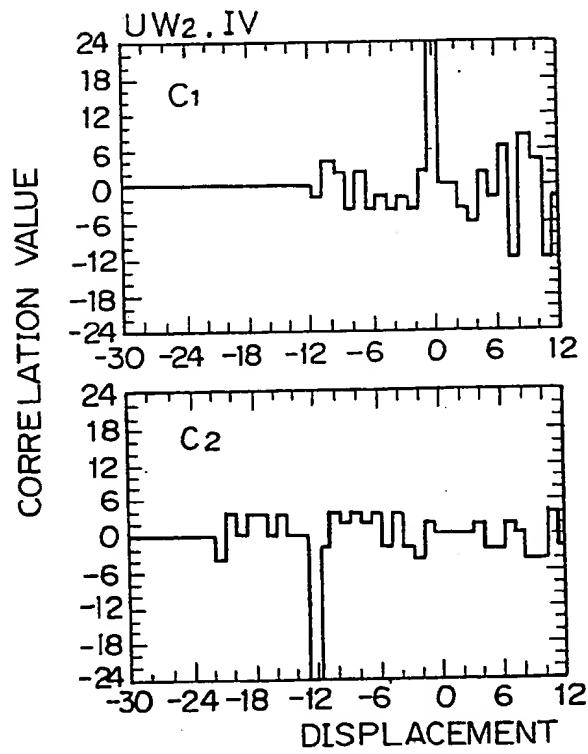


Fig. 4(m)

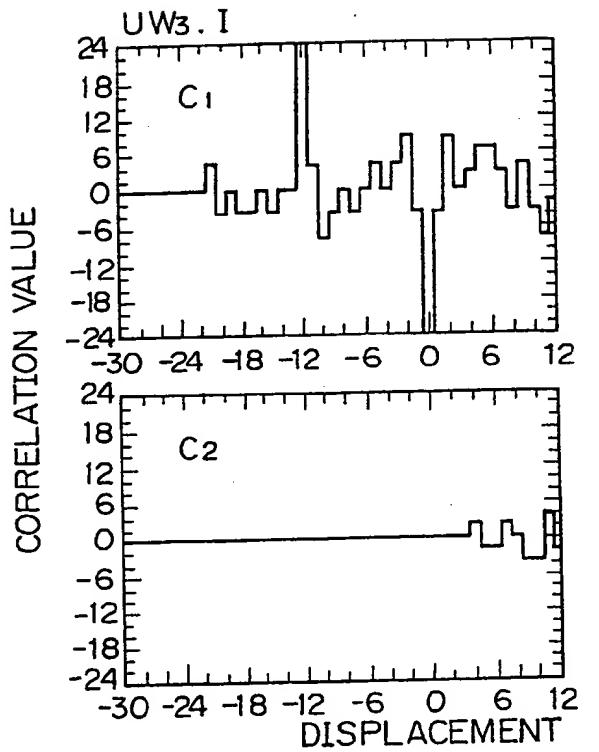


Fig. 4(n)

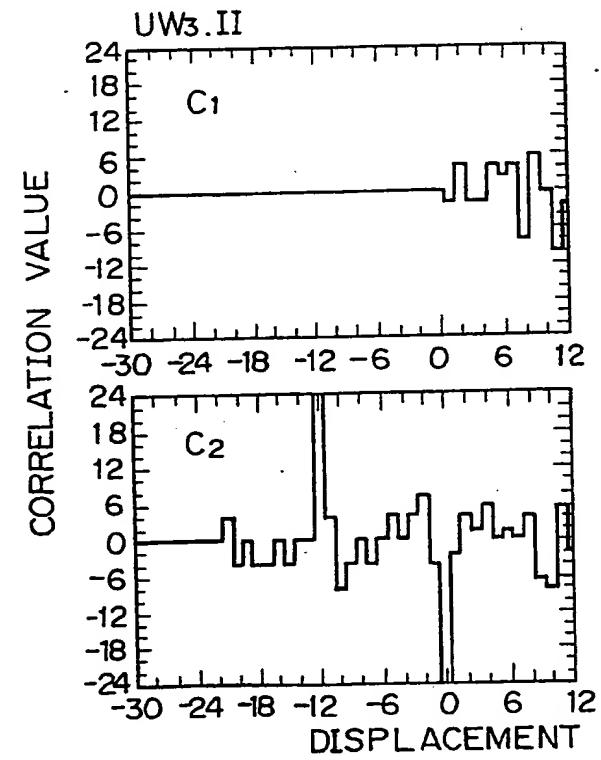


Fig. 4(o)

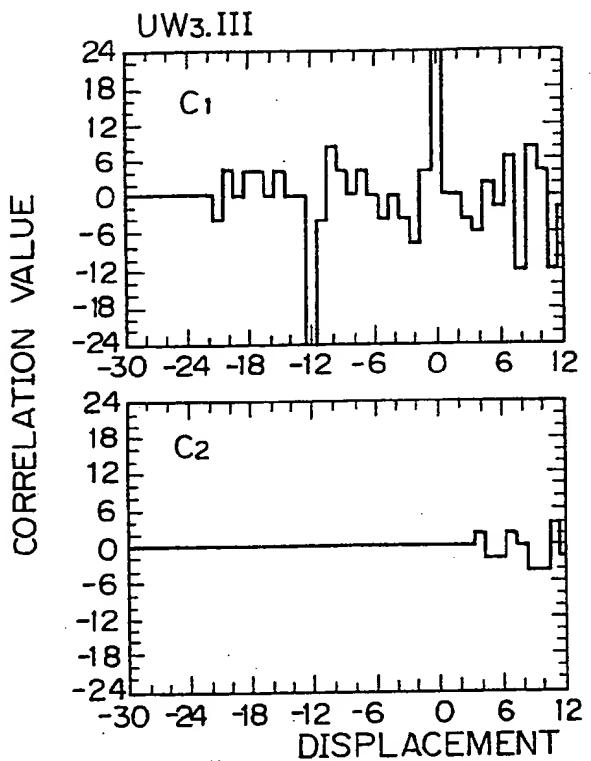


Fig. 4(p)

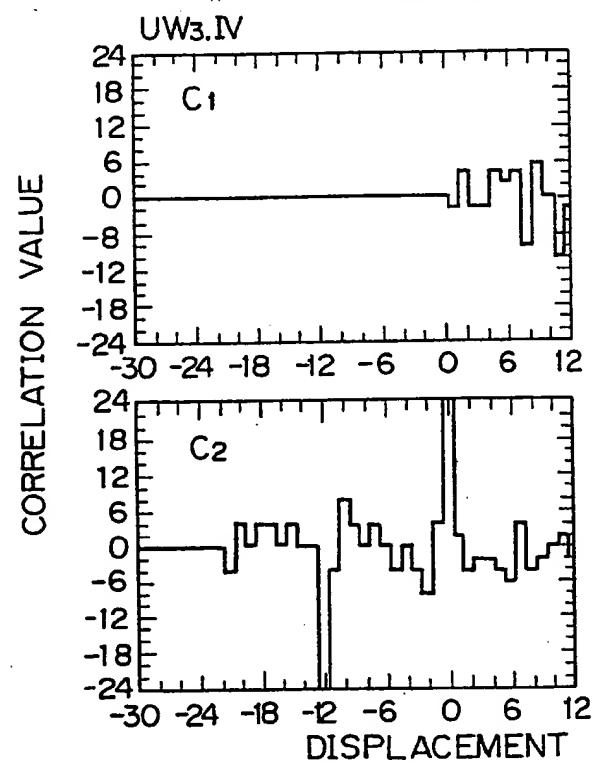


Fig. 5 PRIOR ART

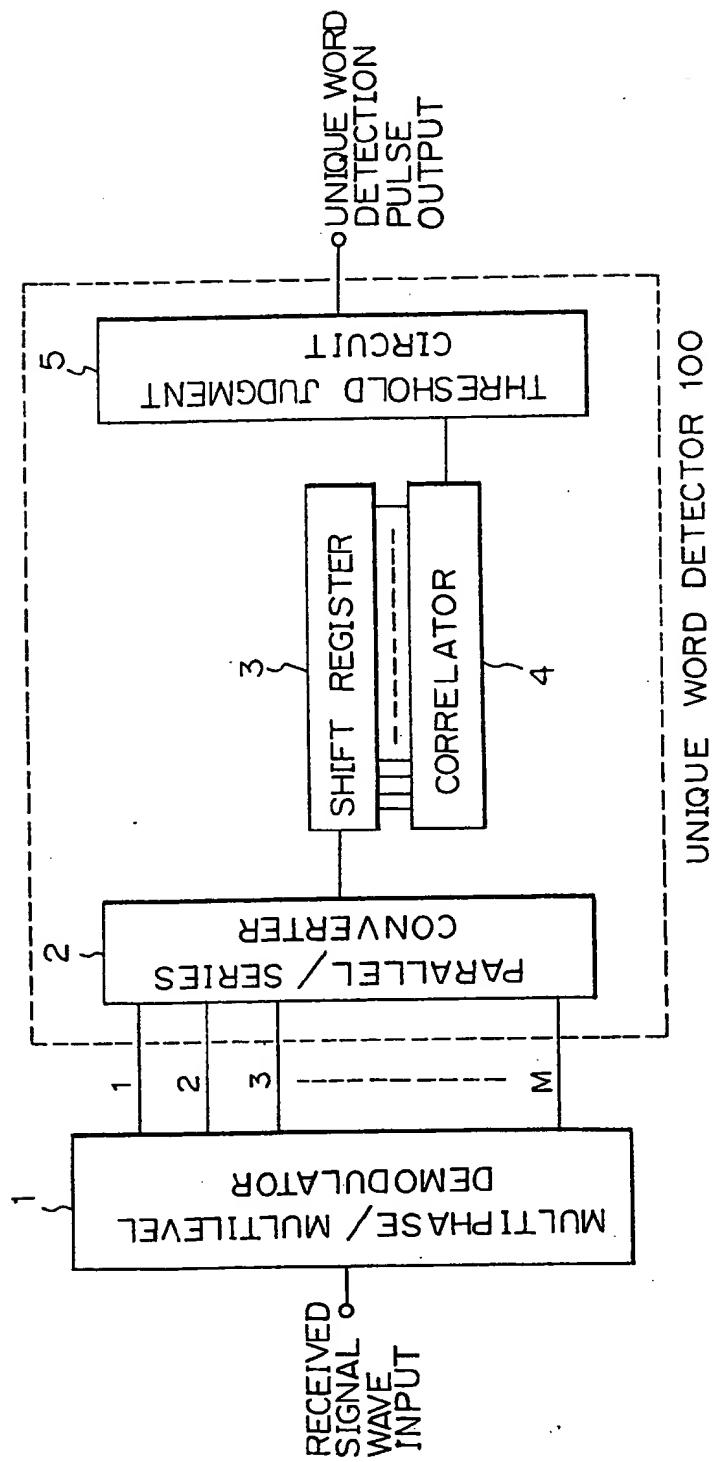


Fig. 6 PRIOR ART

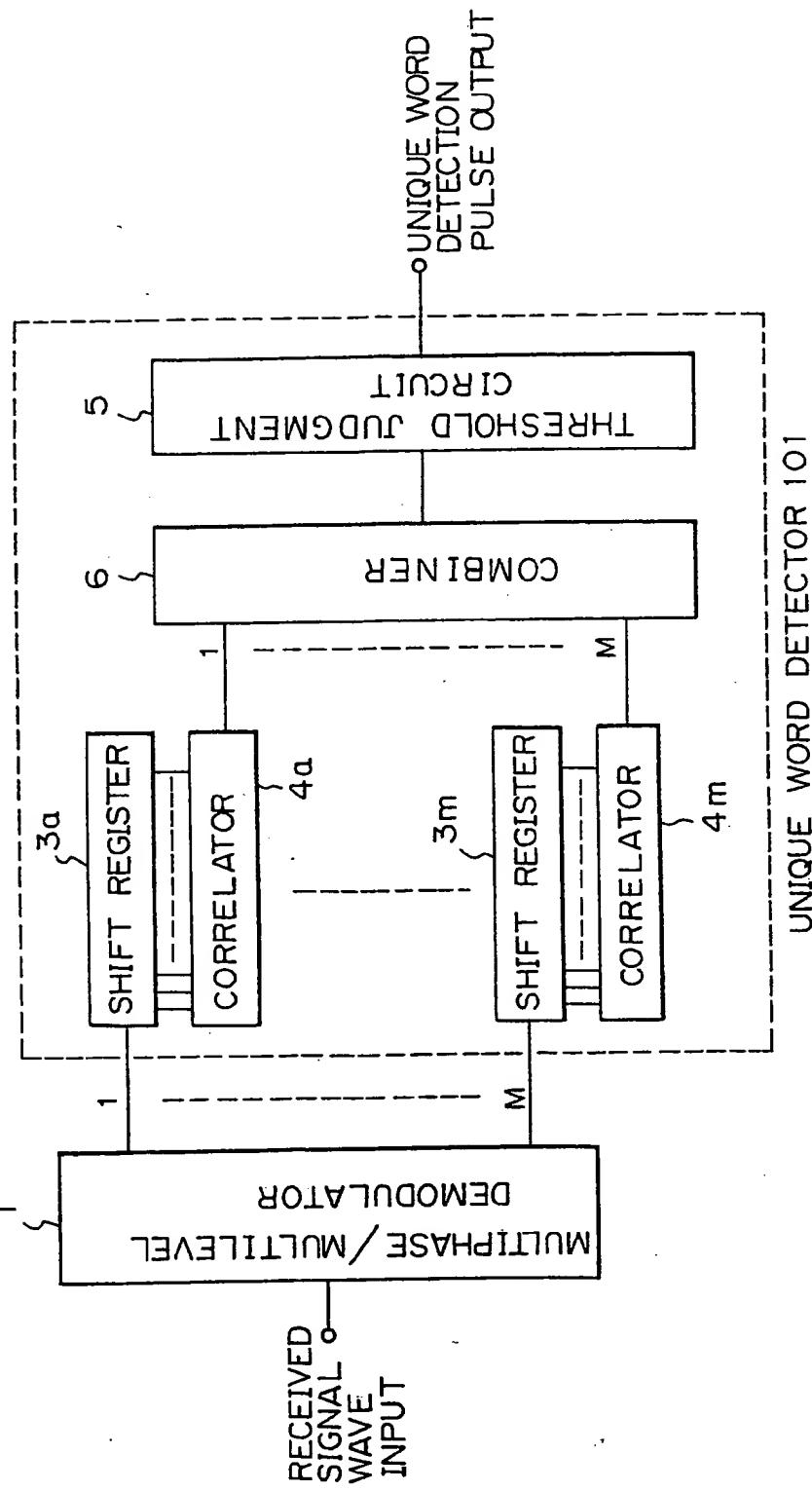


Fig. 7 PRIOR ART

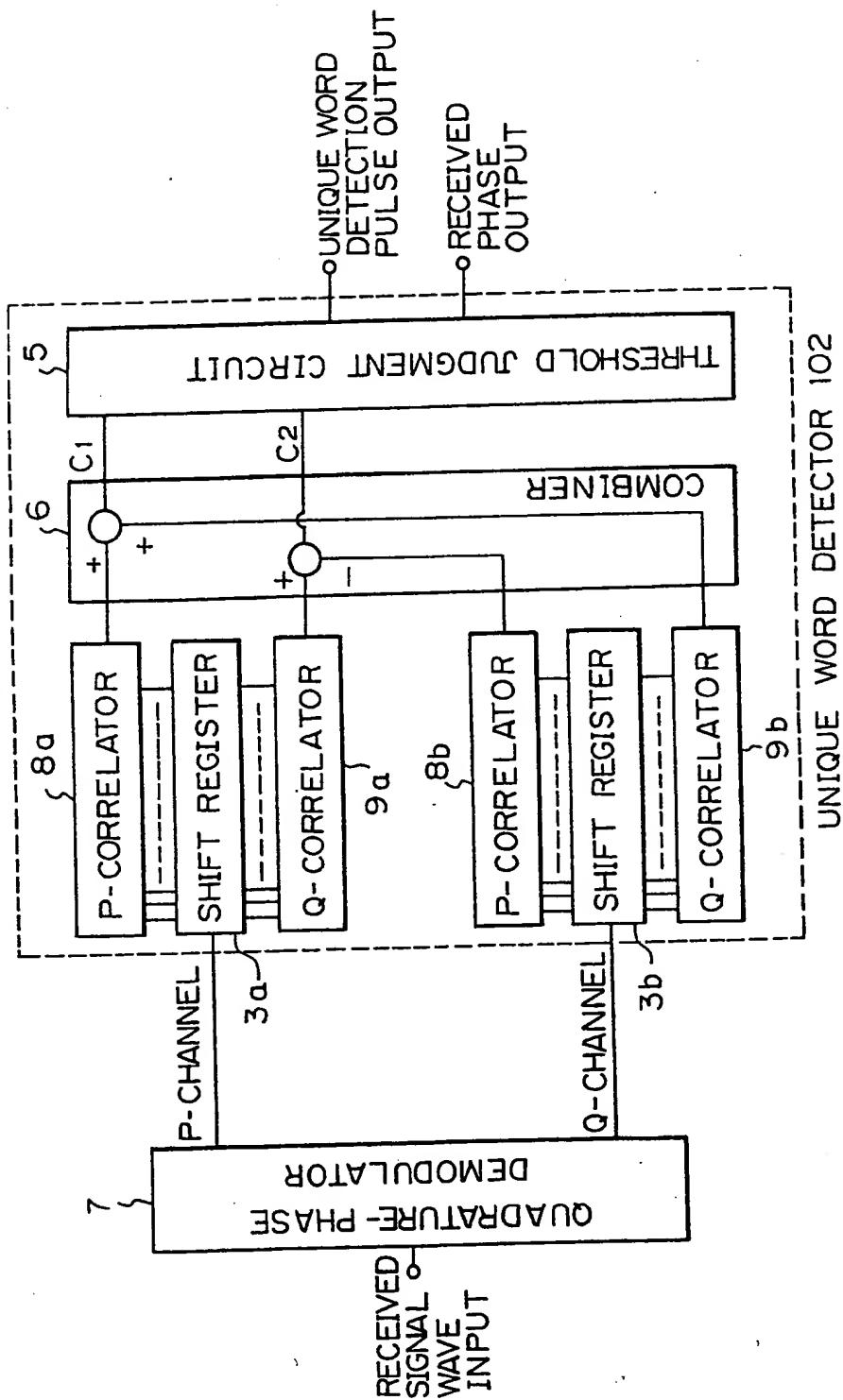
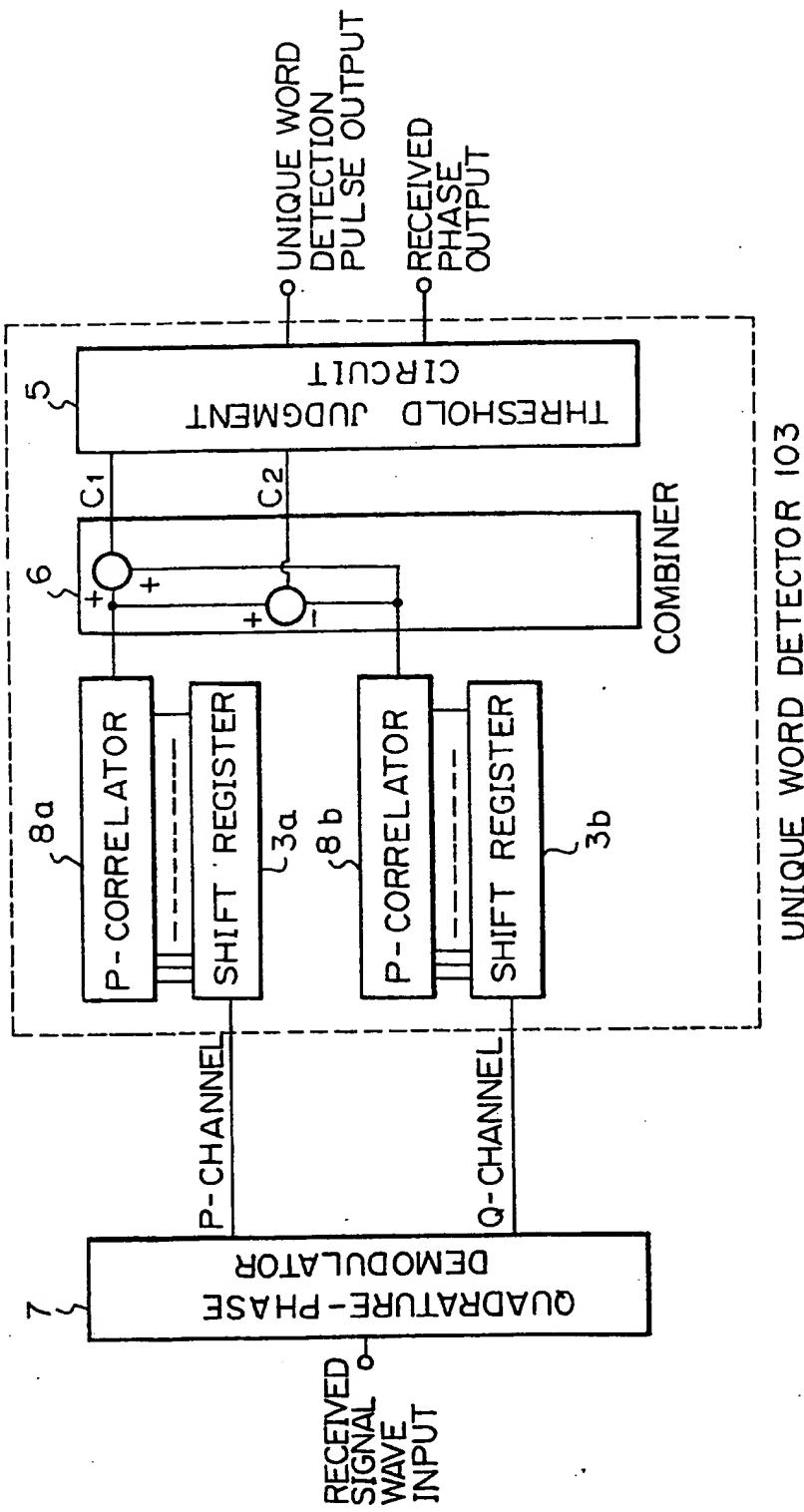


Fig. 8 PRIOR ART



UNIQUE WORD DETECTOR 103



(19) Europäisches Patentamt
European Patent Office
Office européen des brevets



(11) Publication number:

0 358 582 A3

(12)

EUROPEAN PATENT APPLICATION

(21) Application number: 89420322.3

(51) Int. Cl.5: H04L 7/04, H04B 7/212

(22) Date of filing: 04.09.89

(30) Priority: 07.09.88 JP 222275/88

(43) Date of publication of application:
14.03.90 Bulletin 90/11

(84) Designated Contracting States:
FR GB

(88) Date of deferred publication of the search report:
09.10.91 Bulletin 91/41

(71) Applicant: KOKUSAI DENSHIN DENWA CO.,

LTD

3-2, Nishi-shinjuku 2-Chome
Shinjuku-ku Tokyo(JP)

(72) Inventor: Shinonaga, Hideyuki
10-3 Kurihara 4-Chome
Niza-shi Saitama(JP)

(74) Representative: de Beaumont, Michel
1bis, rue Champollion
F-38000 Grenoble(FR)

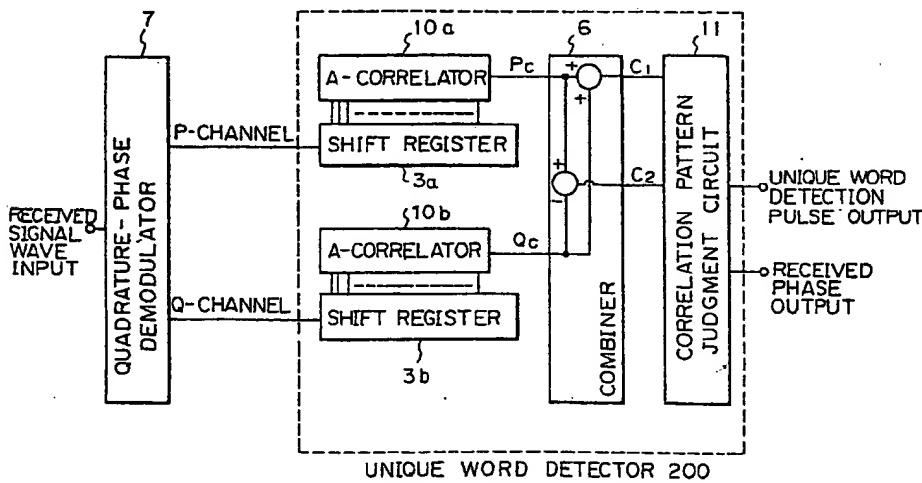
(54) A unique word detection system.

(57) A unique word and/or a receive phase in the demodulator in a multi-phase signal and/or a multi-level signal is detected by obtaining a plurality of correlation values, performing linear calculations for those correlation values to provide a correlation pattern, and looking at a table if this pattern coincides

with one of the predetermined patterns. The invention is useful even when a received signal quality is poor and/or the length of the unique word is short. A plurality of unique word, together with received phase are also recognized by the present invention.

Fig. 1A

EP 0 358 582 A3





European
Patent Office

EUROPEAN SEARCH
REPORT

Application Number

EP 89 42 0322

DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)		
A	TRANSACTIONS OF THE INSTITUTE OF ELECTRONICS AND COMMUNICATION vol. E64, no. 9, September 1, 1981, TOKYO JP pages 595 - 601; A.OGAWA AND AL: 'A UNIQUE WORD DETECTION METHOD AND ITS ERROR RATE PERFORMANCE' * page 595, right column, line 18 - line 21 ** page 596, right column, line 14 - line 21; figure 2 * -----	1-10	H 04 L 7/04 H 04 B 7/212		
TECHNICAL FIELDS SEARCHED (Int. Cl.5)					
H 04 L H 04 B					
The present search report has been drawn up for all claims					
Place of search	Date of completion of search	Examiner			
The Hague	07 August 91	CRETAINE P.A.			
CATEGORY OF CITED DOCUMENTS					
X: particularly relevant if taken alone					
Y: particularly relevant if combined with another document of the same category					
A: technological background					
O: non-written disclosure					
P: intermediate document					
T: theory or principle underlying the invention					
E: earlier patent document, but published on, or after the filing date					
D: document cited in the application					
L: document cited for other reasons					
&: member of the same patent family, corresponding document					